CS 472 Computer Architecture, Sections EX/D1

Spring Semester 2019 / On Campus – EPC 208, 750 Comm. Ave / Thursday night 6-8:45 p.m.

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Text: David A. Patterson and John L. Hennessy
        Computer Organization & Design: The Hardware/Software Interface
        Fifth Edition (ISBN 978-0-12-407726-3)
        Available at the BU bookstore or online. No other edition is acceptable!

Prerequisites: MET CS231 or CS232 **Do not ignore this prerequisite!** You need to be proficient in some language, comfortable handling arrays and classes of data. Some assembly language is helpful but not mandatory.

Description: Computer organization with emphasis on processors, memory, and input/output. Includes pipelining, ALUs, caches, virtual memory, parallelism, measuring performance, and basic operating systems concepts. Discussion of assembly language instruction sets and programming as well as internal representation of instructions. (In short, enable you to understand the internal workings of computers, what makes them fast, and what the primary design challenges are.)

Grades: There will be a midterm (30%) and a final (40%) which will together account for 70% of the grade. There will be also three programming projects (in the language of your choice) for the other 30%. Late submissions will be accepted with a 10% pro-rated penalty for each week late *except for the final project*.

In general, an "A" will be awarded for work that totals 92-100% of the possible points, "A-" for 90-92%, "B+" for 88-90%, "B" for 82-88% and so on down to F for below 60%. Grades may be scaled upwards based on class scores (though rarely to get an A), but not downwards.

Grades are YOUR responsibility. If you need a particular grade to get into the M.S. program, receive tuition reimbursement or stay academically eligible, then it is YOUR responsibility to perform at that level. "A" work will get you an "A" and "F" work (or cheating) will get you an "F". I must distinguish between exceptional work and that which falls short of that level. **Grades of D or F are almost always the result of cheating or not doing projects.**

Policies: Attendance is not a part of your grade. However, you are expected to take the exams at the scheduled time. If that is impossible, you must take the exam before the rest of the class. If you are a "no show" you get a zero.

Exams are closed book, with no notes. However, reference material from the text is provided to make the test one of your understanding rather than photographic memory. The focus is on thorough comprehension of the concepts, not mindless memorization of trivia.

Ethics: I try to be friendly and to inject humor into the lectures, but don't mistake that for anything less than a zero-tolerance policy toward cheating. YOU CHEAT, YOU FAIL, subject to procedural review.

All projects in this course are INDIVIDUAL. Feel free to discuss ideas with your classmates, but **any** shared code will mean an F for the course. **No exceptions!** It is not acceptable for someone else to do your work, whether that be a classmate, a spouse or anyone. Do not share even one line of code.

Incompletes: Incompletes will be given only in the case of serious emergencies or other documented extenuating circumstances clearly beyond the student's control. Incompletes will NOT, repeat NOT, be given to students who abandon the class, students not satisfied with their grade, students who are too busy to complete the work on time or students who miss the final exam.

Availability: I will do my best to make myself available to you. I have provided my email address and phone number and encourage you to use them as long as you have consulted your textbook and notes first.

Weather: Call (617) 353-SNOW if you have any questions about whether class will be held.   If that number says that BU is open, we will have class.

Backups: Buy a thumb drive (they are cheap) and **back up your work on projects daily**. There are no project extensions or late penalties waived for a disk crash. (Or just email the code to yourself.)

SCHEDULE

Date	Topics	Related Reading
Jan 23	Course Overview, Number systems, Signed numbers, Characters	Chap. 1.2, 2.4, 2.5, 2.9, skim rest Chap 1
Jan 31	MIPS Instructions - Assembly Language and Internal Representation *First project assigned: Due Feb 21*	Chapter 2 (skip asm programming, focus on internal rep)
Feb 7	Measuring Computer Performance, CISC/RISC The Memory Hierarchy - Caches (Part 1)	Chapter 1.6 Chap. 5.1, 5.3, 5.4 (ignore math), 5.8
Feb 14	The Memory Hierarchy - Caches (Part 2), *Second project assigned: Due Mar 21*	
Feb 21	The Processor: Datapath and Control	Chapter 4.1 - 4.4
Feb 28	MIDTERM EXAM	
Mar 7	Pipelines (Part 1)	rest of Chapter 4
Mar 14	Spring Break	
Mar 21	Pipelines (Part 2) *Third project assigned: Due Apr 25*	
Mar 28	Advanced Architectures - Superscalar, Superpiplining	
Apr 4	Parallelism, Multicores, OS Concepts, Virtual Memory, Start-of-the-Art Cache extensions	Chapter 6, Chap 5.6
Apr 11	Catch up	
Apr 18	Logic Design, Computer Arithmetic and ALUs	App. B: pp. 1-14
Apr 25	Final Exam Review	
May 2	NO CLASS: Study Period	
May 9	FINAL EXAM	