

Adaption of Cheon-Kim-Kim-Song (CKKS) for Practical Use in Computer Systems

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Introduction

- Traditionally encrypted data cannot be altered without decryption, limiting privacy and cloud computing, where third parties benefit from processing data without viewing it.
- Homomorphic Encryption (HE) allows mathematical operations on ciphertext, overcoming this limitation. Refer to the image for details. However, HE is impractical due to slow run time.

Objective

- This study aims to improve the efficiency of Cheon-Kim-Kim-Song (CKKS), a HE algorithm, on a microarchitecture level

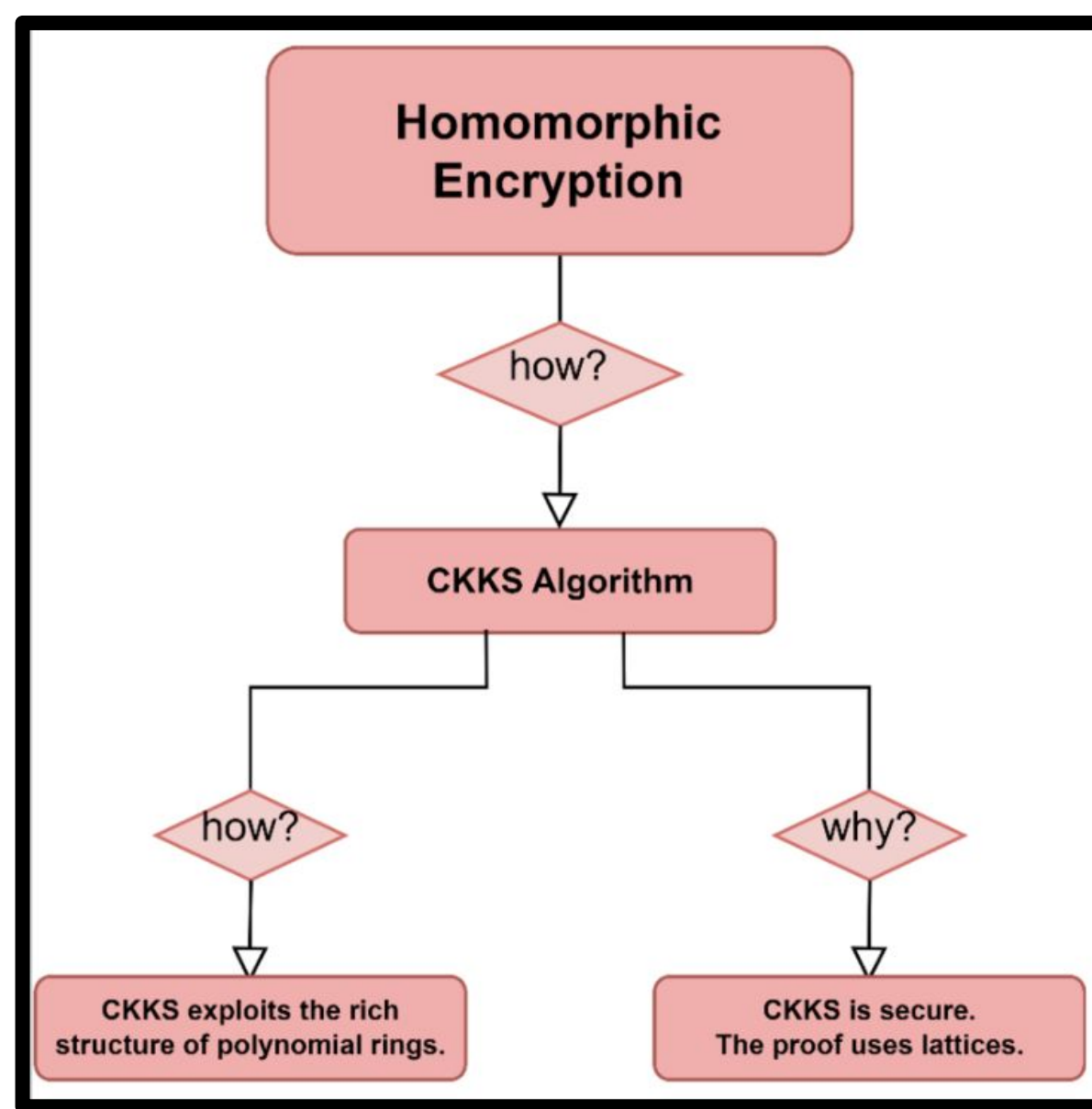


Figure 1. CKKS Algorithm Walkthrough

CKKS

- CKKS is a fully homomorphic encryption (FHE) system

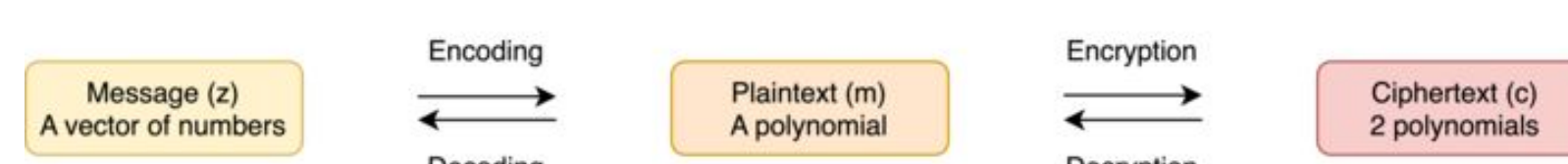


Figure 2. CKKS Algorithm Encryption and Decryption

Methods

Overview

- To test variable factors, the training runtime of various Neural Networks were compared

Neural Network

- Neural Network is trained on MNIST Database, database of hand drawn numbers
- The Neural Network structure is shown

MNIST Example

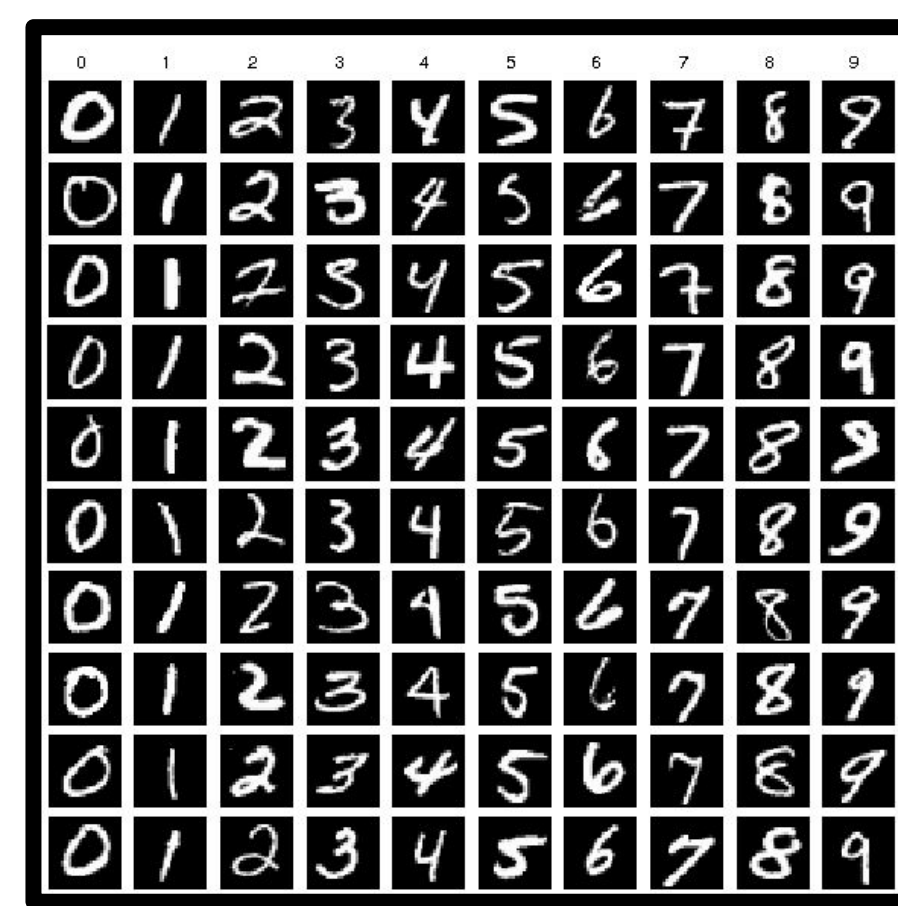


Figure 4. Example images in MNIST Database

Approximations

- CKKS supports limited operations, mainly addition and multiplication, enabling polynomial evaluations.
- Functions like ReLU, SoftMax, and Loss cannot be directly performed and must be approximated with Chebyshev polynomials.

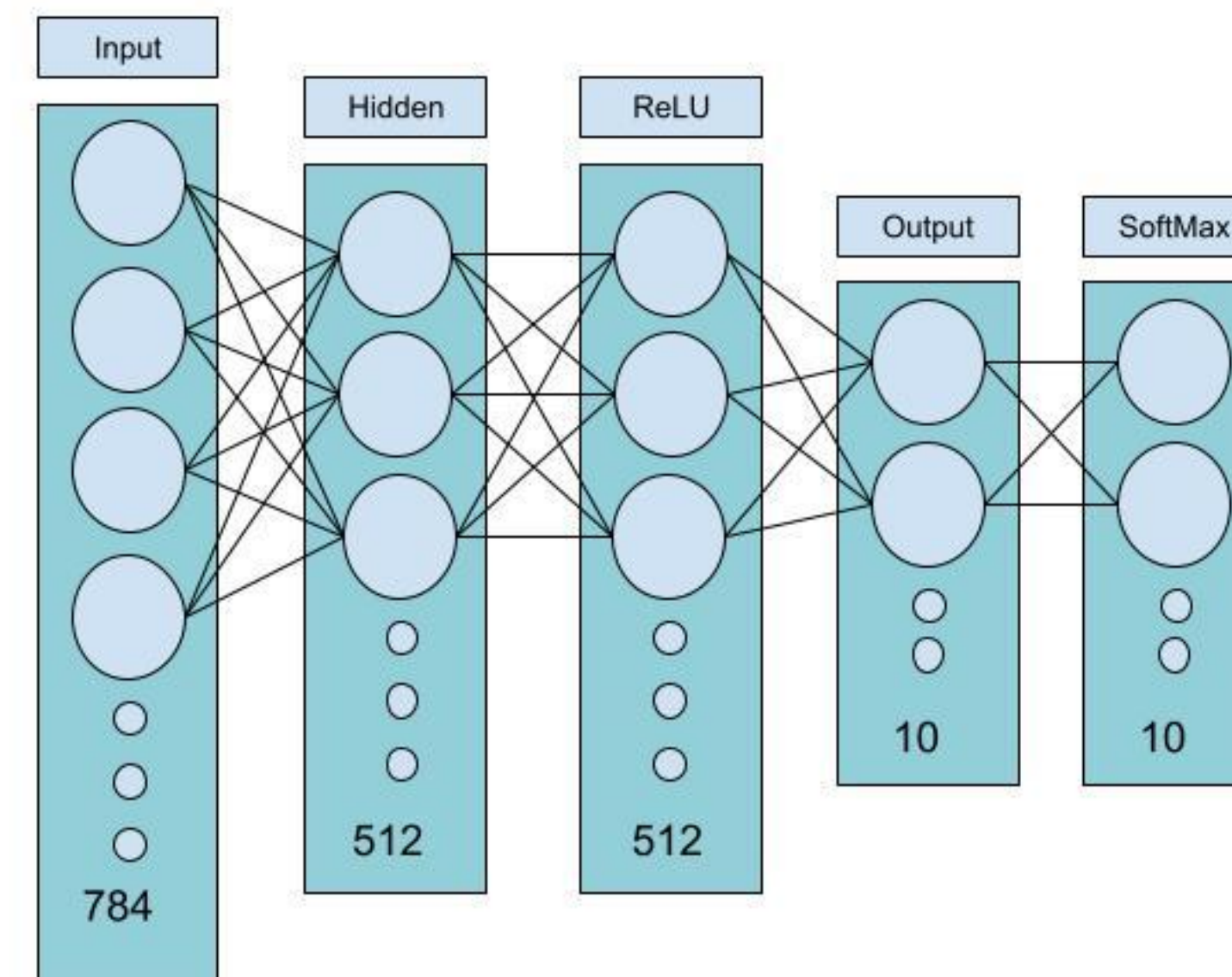


Figure 3. Neural Network Base Architecture

Variable Factors

- Computer Architecture:** The two most common computer architectures, ARM and x86 each have layouts that either that could benefit or detriment the runtime.
- Vector Parallel Processing:** Parallel Processing would cause multiple vectors to be processed at once, possibly increasing speed. However, overhead cost cache coherence, and false sharing could cause an algorithm to take more time when run in parallel.

Experiment

- The experiment with consist of 6 Neural Network Variations
- Each of these networks were run 10 times and were recorded and compared

	ARM	x86
Traditional	Model 1	Model 2
CKKS	Model 3	Model 4
CKKS + Vector Optimization	Model 5	Model 6

Figure 5. Neural Network Variation Classification

Discussion/Conclusions

Figure 12. Architecture Scale Factor Application

Results

- Figure above shows design specifications for the Computer used in each Architecture
- Scale Factor:** 1.84

Runtime Improvements

- For traditional encryption, Model 2 significantly outperformed Model 1 with a 2.63X performance, scaled to 1.43X performance boost for x86 compared to ARM
- Applying encryption to network appraised approximately a 6417X slowdown from performance overhead
- Applying Vector Parallelization with x86 gave approximately a 5.67X performance increase, as well as an approximately 2.45X performance increase with ARM

Conclusion: x86 architecture is more effective with the application of Cryptography. Vector Parallelization is also effective with runtime decrease, the most effective being with x86.

Results

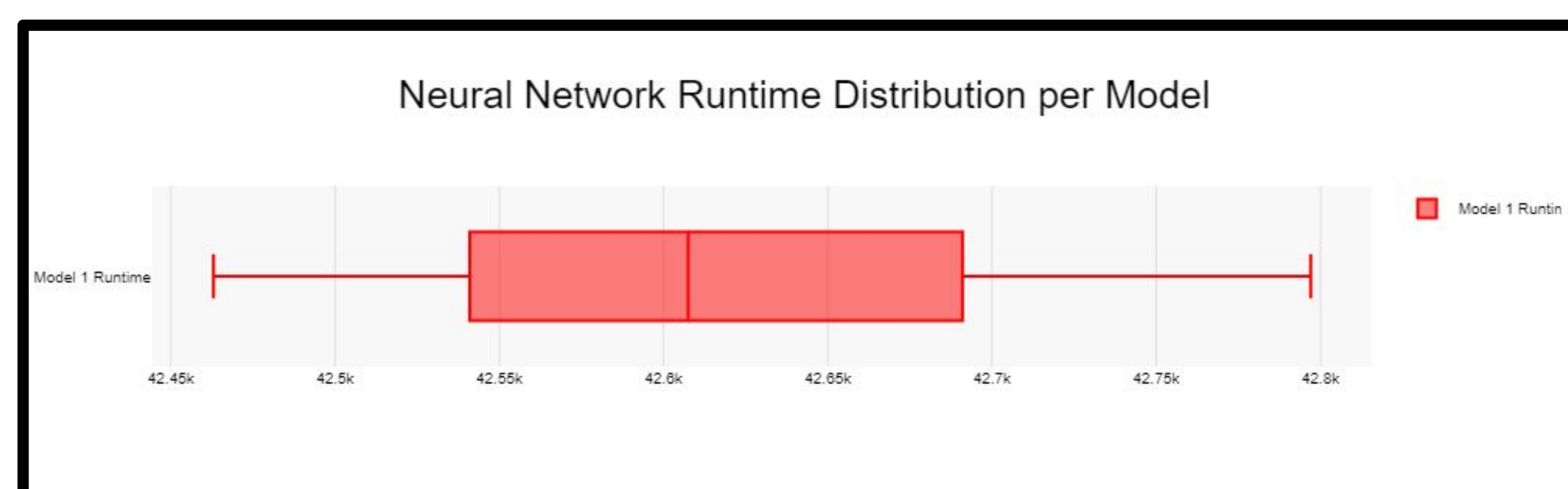


Figure 6. Model 1 Runtime Distribution

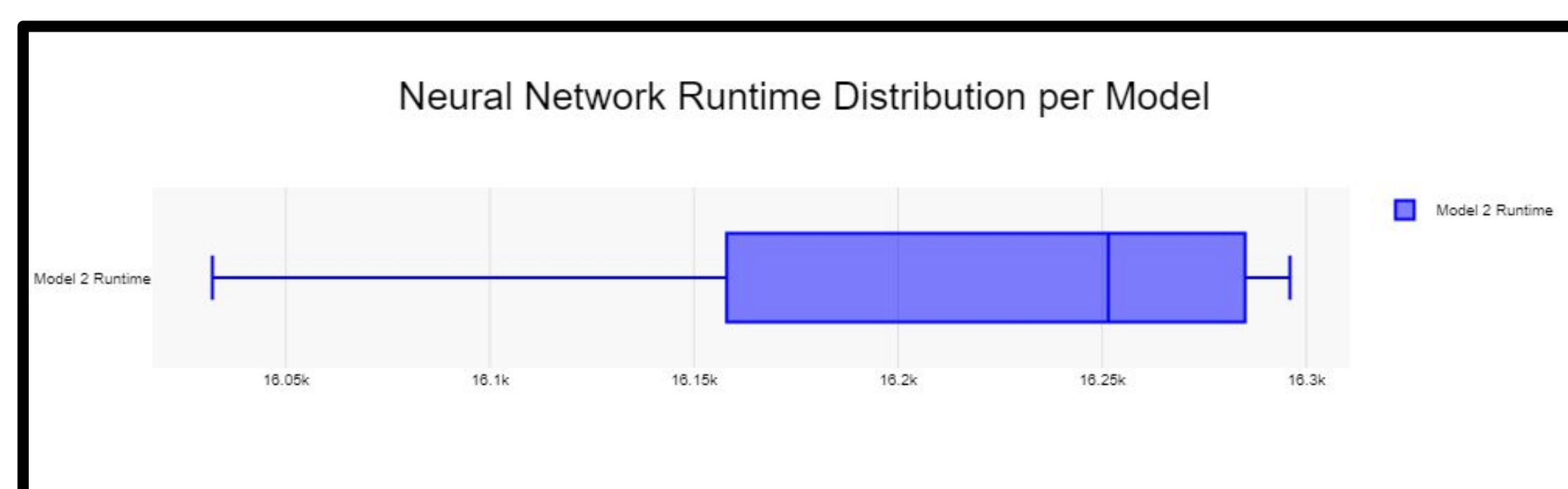


Figure 7. Model 2 Runtime Distribution

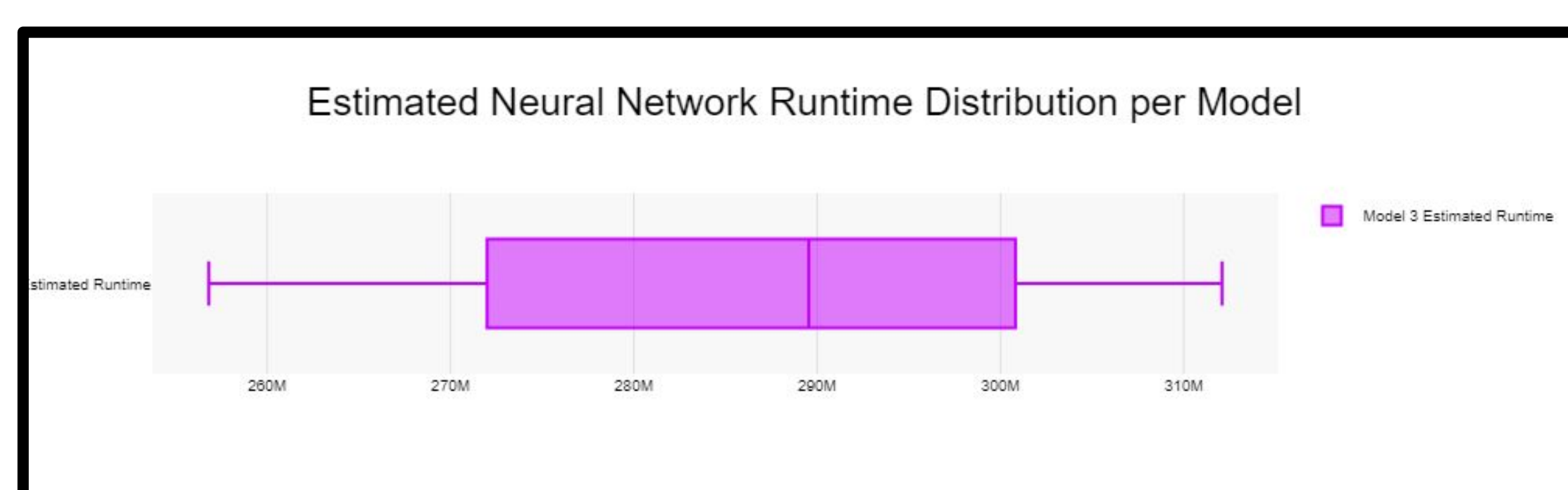


Figure 8. Model 3 Runtime Distribution

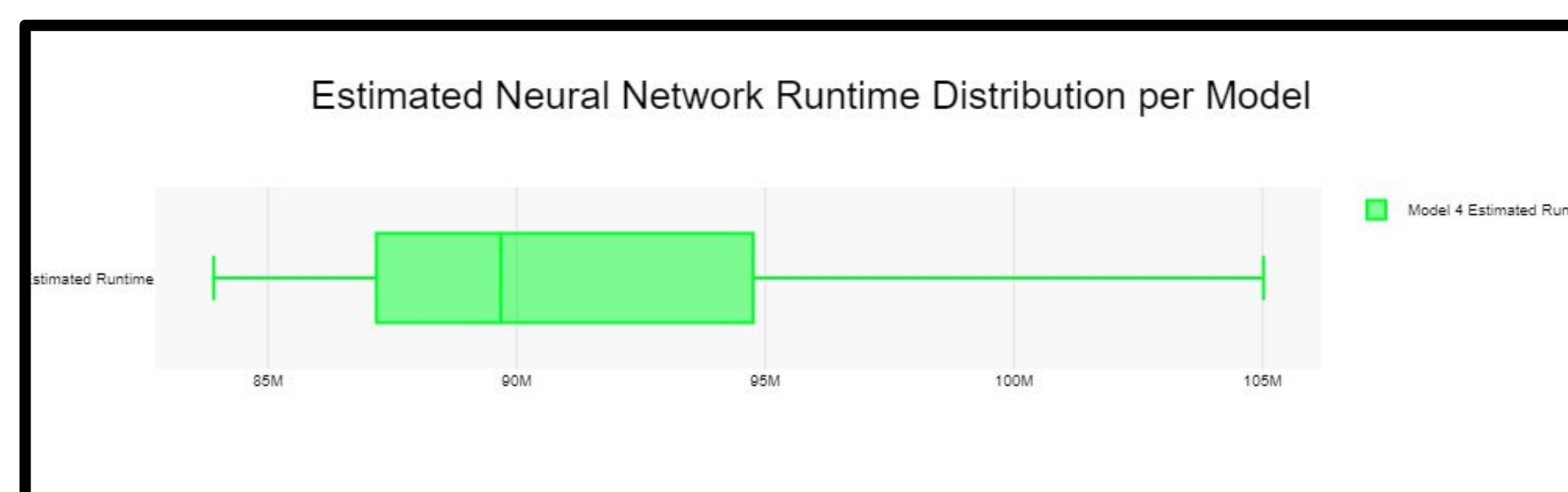


Figure 9. Model 4 Runtime Distribution

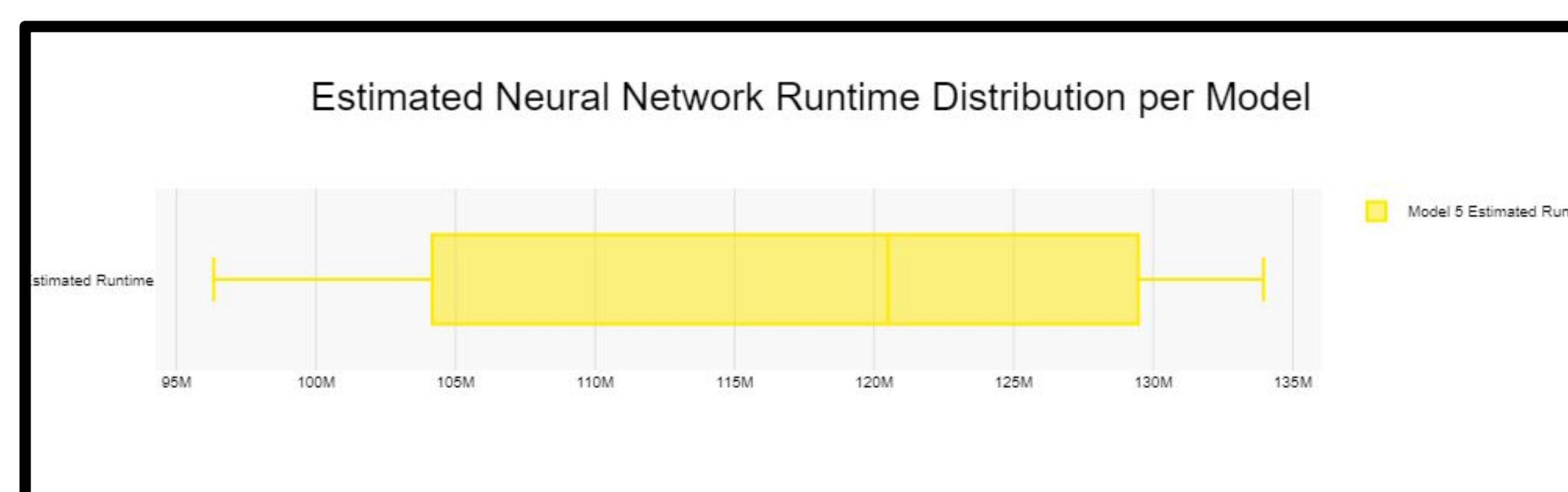


Figure 10. Model 5 Runtime Distribution

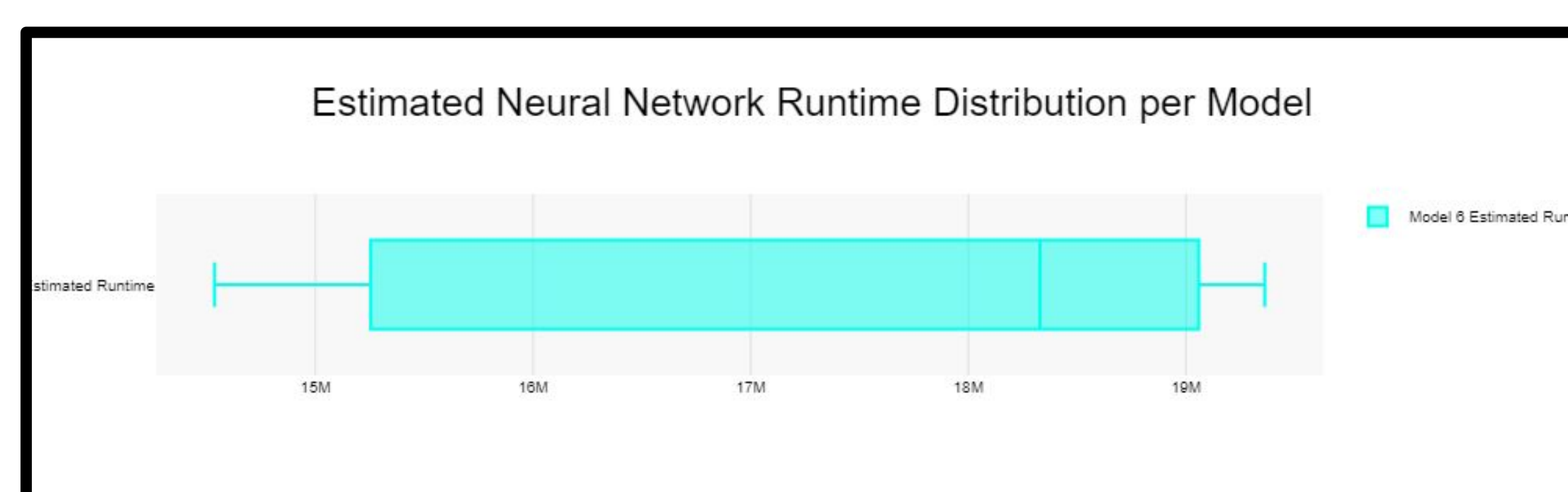


Figure 11. Model 6 Runtime Distribution

References

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- keywords: {Chebyshev approximation;Neural networks;Least squares approximation;Recurrent neural networks;Polynomials;Feedforward neural networks;Multi-layer neural network;Multilayer perceptrons;Least squares methods;Computer errors},

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